

FIG. 1

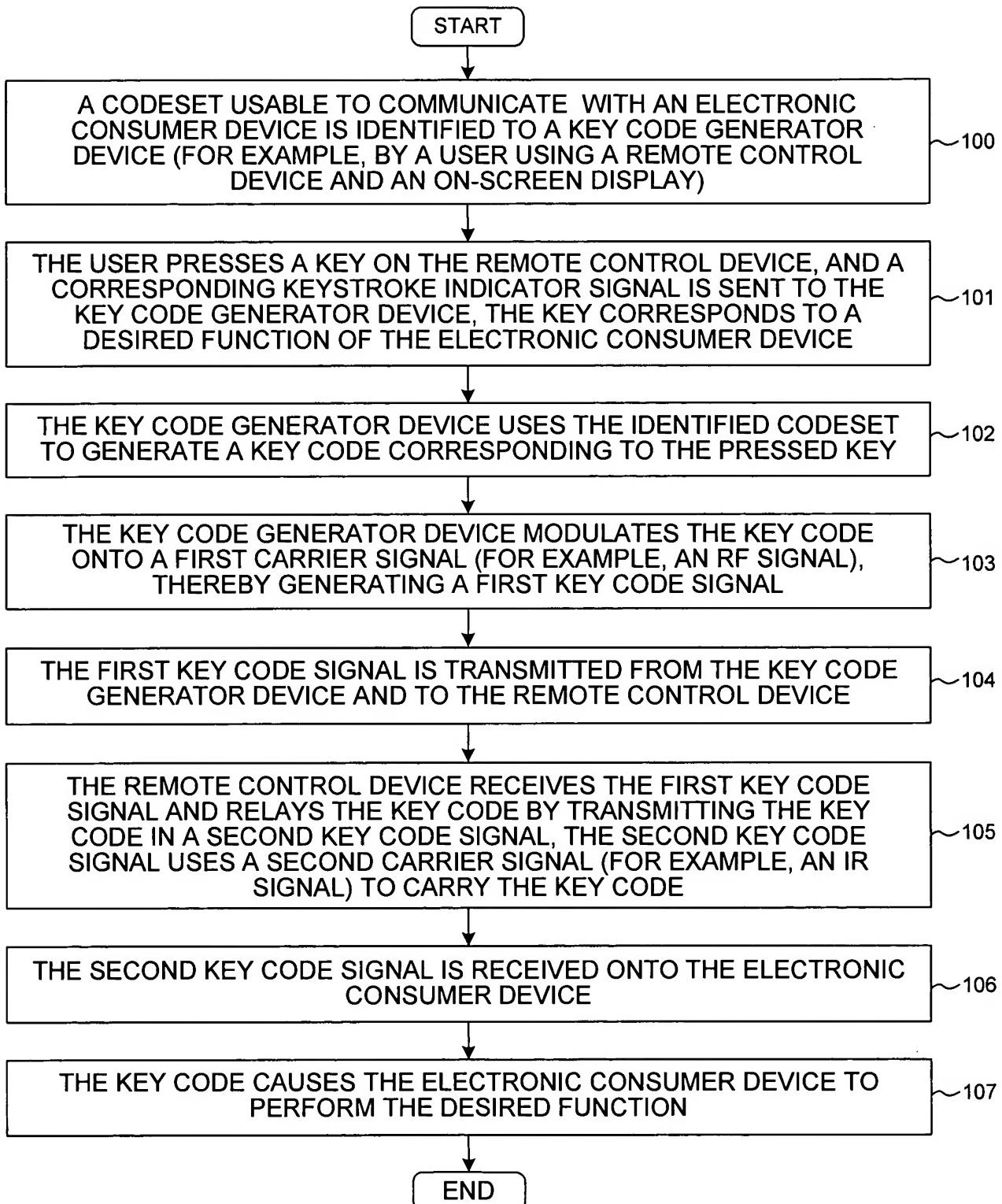


FIG. 2

3/4

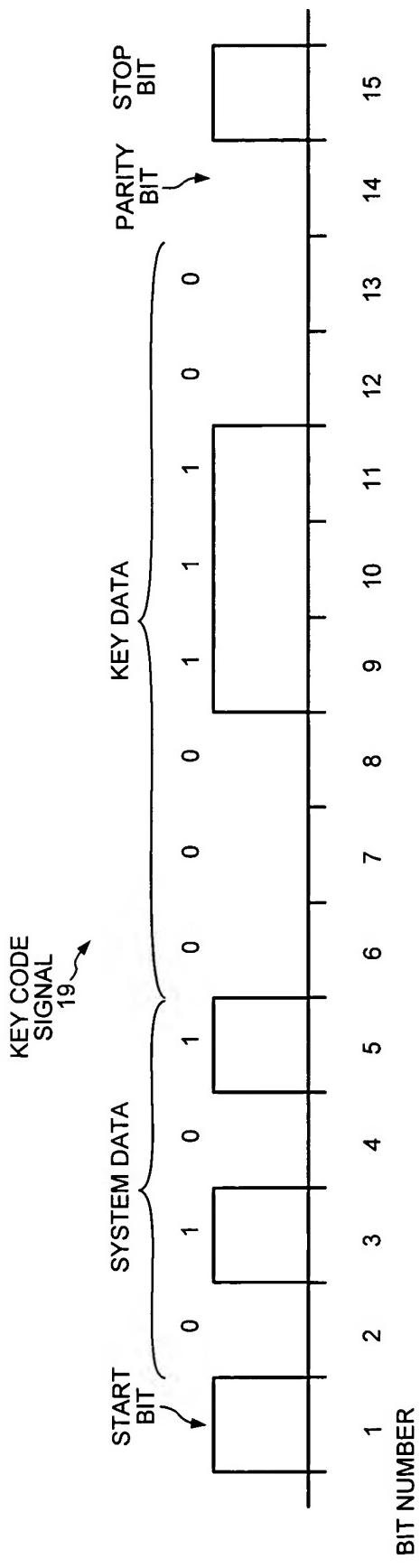


FIG. 4  
BINARY TRANSMISSION

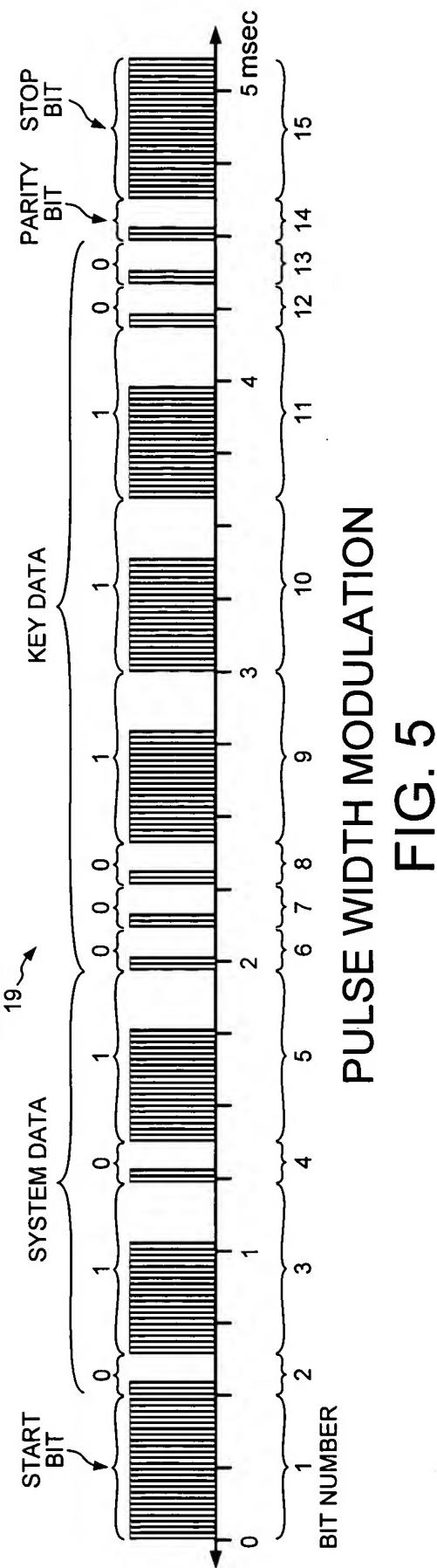


FIG. 5  
PULSE WIDTH MODULATION

4/4

0 1 0 1 0 0 0 1 1 1 0 0

{ } { }  
SYSTEM CODE KEY DATA

KEY CODE  
FIG. 3

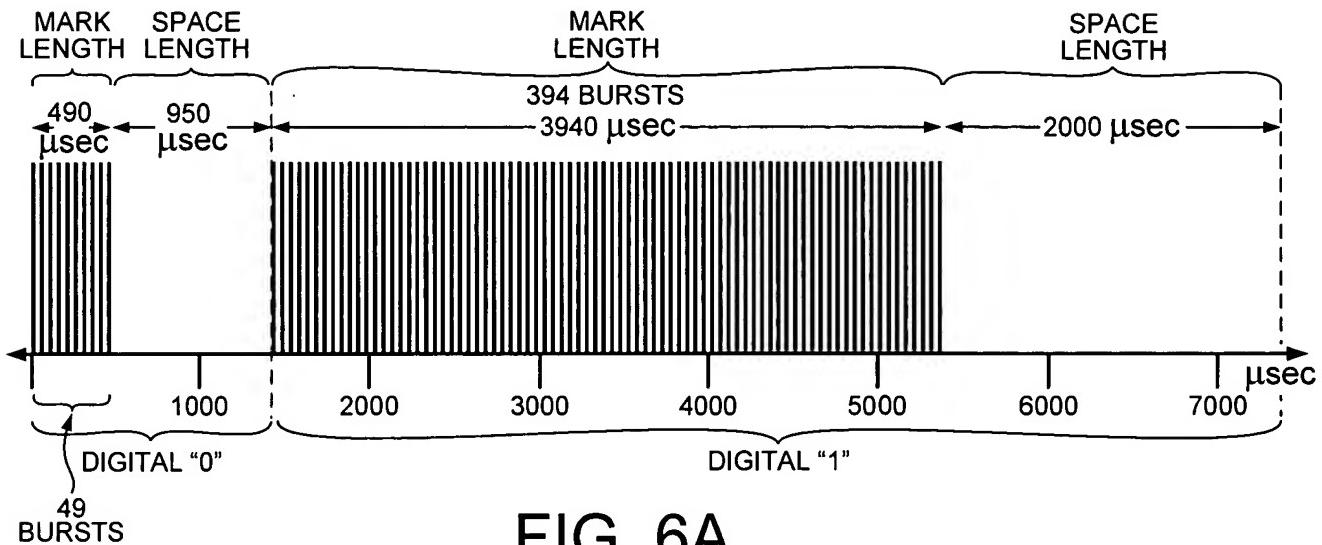


FIG. 6A

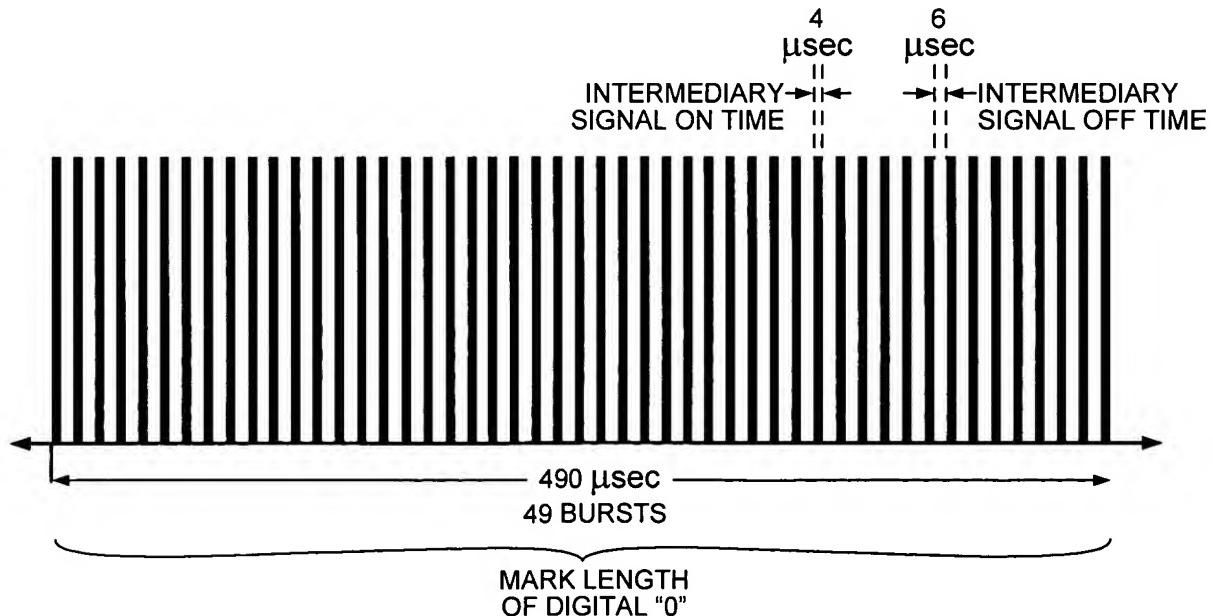


FIG. 6B